

Art Unit: ***

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CLAIMS 1 – 49 (CANCELLED)

50. (new) An electronic system utilizing a host device and removable memory, comprising:

a first electronic circuit card having first and second electrical connectors thereon and including a controller function therein that is operable to control a memory when the memory is connected to the second connector and to interface with the host when the host is connected to the first connector,

a second electronic circuit card without the controller function and including non-volatile reprogrammable memory therein connected to a third electrical connector that mates with the second electrical connector of the first card for control thereof by the controller function within the first card,

a first receptacle within the host that mates with the first electrical connector of the first card,

a second receptacle within the host that mates with the third electrical connector of the second card, and

the host additionally includes the controller function connected to the second receptacle to control the memory of the second card when the third connector mates with the second receptacle but without the controller function being connected to the first receptacle,

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whereby the second card containing memory may be connected directly with the host by insertion into the second host receptacle or indirectly by connection with the first card containing the controller function that is in turn inserted into the first host receptacle.

51. (new) The electronic system of claim 50, wherein an interface at the mating first host receptacle and first connector of the first card is a standard PCMCIA interface.

52. (new) The electronic system of claim 50, wherein the host includes a personal computer.

53. (new) An electronic host utilizing non-volatile reprogrammable memory of a type that uses a controller to operate the memory and interface it with the host, comprising:
a first receptacle adapted for insertion of a first circuit card containing a first non-volatile reprogrammable memory and a first controller to operate the first memory within the card and interface it with the host,
a second receptacle adapted for insertion of a second circuit card containing a second non-volatile reprogrammable memory without a controller to operate the second memory or interface it with the host, and
a second controller within the host and connected to the second receptacle to control the second memory within the second card.

54. (new) The combination of claim 53 wherein the electronic host includes a personal computer.

55. (new) The combination of claim 54, wherein the host communicates through the first receptacle according to a PCMCIA interface.

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